

*IN THE UNITED STATES PATENT AND TRADEMARK OFFICE*

Applicant: David CASEY  
Title: BIPOLAR TRANSISTOR WITH A LOW SATURATION VOLTAGE  
Appl. No.: 10/566,813  
International Filing Date: 7/12/2004  
371(c) Date: 1/30/06  
Examiner: Jae Lee  
Art Unit: 2895  
Confirmation Number: 8053

AMENDMENT AND REPLY UNDER 37 CFR 1.116

Mail Stop RCE  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Sir:

This communication is responsive to the Final Office Action dated May 19, 2009, concerning the above-referenced patent application.

Applicant has enclosed with this amendment a Petition for Extension of Time to make this response timely.

Amendments to the Claims are reflected in the listing of claims which begins on page 2 of this document.

Remarks/Arguments begin on page 4 of this document.

Please amend the application as follows: